

Serial No. 10/091,432

Claim 14 recites "a delay circuit for receiving the analog signal, and delaying the analog signal corresponding to a latency caused by the first control loop." Qureshi does not teach this feature. The Examiner asserts that column 4, lines 8-20 of Qureshi disclose this feature. However, as explained above, neither the text in column 4 nor any other portions of Qureshi disclose a feature comparable to this feature of claim 14.

Claims 2-5 and 8-13 depend upon claims 1 and 7 respectfully. Thus, claims 2-5 and 8-13 are patentable for at least the reasons their base claims are patentable.

Response to § 103 Rejections

The Examiner rejected claim 8 under 35 U.S.C. § 103(a) as unpatentable over Qureshi in view of U.S. Patent No. 6,563,859 ("Oishi"). The Examiner also rejected claims 12 and 13 under 35 U.S.C. § 103(a) as unpatentable over Qureshi in view of U.S. Patent No. 6,897,724 ("Gurvich"). Additionally, the Examiner rejected claims 9-11 under 35 U.S.C. § 103(a) as unpatentable over Qureshi in view Oishi and further in view of Gurvich. These rejections are respectfully traversed.

Nothing has been cited or found in Oishi or Gurvich that suggests modifying Qureshi to overcome the deficiencies discussed above. Therefore, claims 8-13 are patentable over Qureshi in view of Oishi or Gurvich.

Response to § 112 Rejections

The Examiner rejected claim 10 under 35 U.S.C. § 112, second paragraph. This Amendment responds to this rejection by amending the claim.

Response to Objections

The Examiner objected to claims 3, 14, and 19. This Amendment responds to the objection by amending the claims.

Conclusion

There being no further outstanding objections or rejections, it is submitted that the application is in condition for allowance. An early action to that effect is courteously solicited.

Finally, if there are any formal matters remaining after this response, the Examiner is requested to telephone the undersigned to attend to these matters.